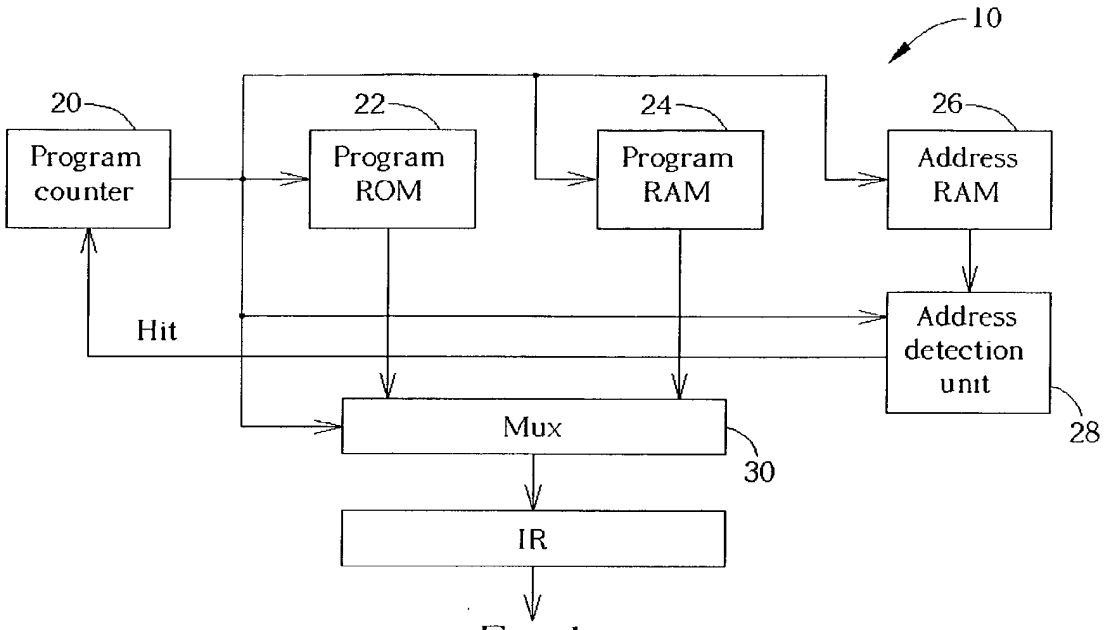
10
20
Program counter
22
Program ROM
24
Program RAM
26
Address RAM
Hit
Address detection unit
28
Mux
30
IR

Fig. 1

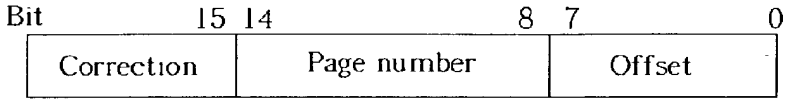

Fig. 2

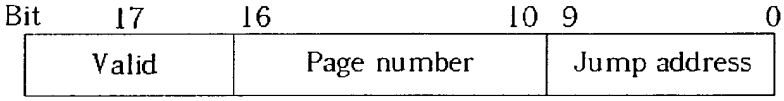

Fig. 3

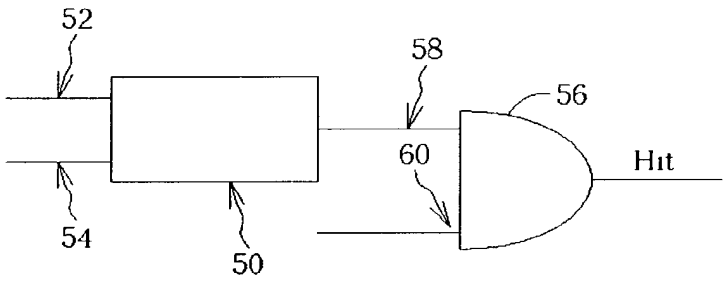

Fig. 4

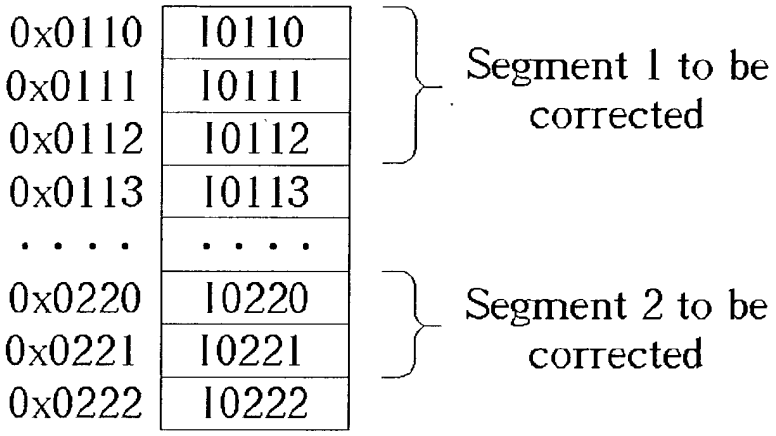
0x0110
0x0111
0x0112
0x0113
. . . .
0x0220
0x0221
0x0222
I0110
I0111
I0112
I0113
. . . .
I0220
I0221
I0222
Segment 1 to be corrected
Segment 2 to be corrected

Fig. 5A

0x10	1	0x01	0x0
0x11	0	. . . .	. . . .
. . . .	. . .	. . . .	. . . .
0x20	1	0x02	0x4
0x21	0	. . . .	. . . .
. . . .	. . .	. . . .	. . . .

Fig. 5B

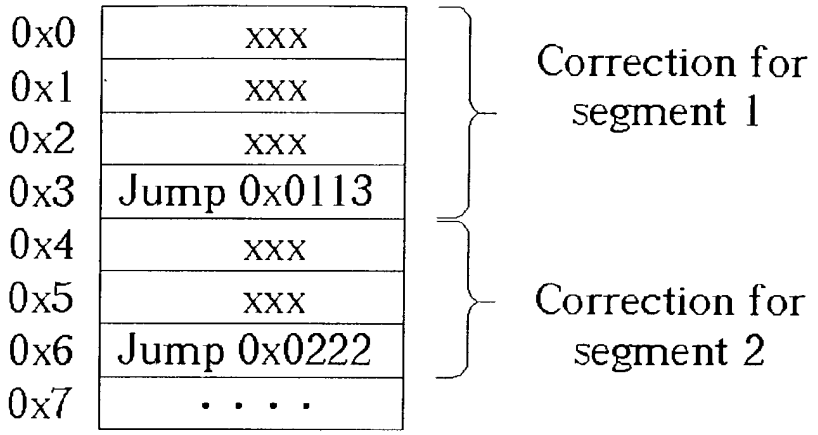
0x0
0x1
0x2
0x3
0x4
0x5
0x6
0x7
xxx
xxx
xxx
Jump 0x0113
xxx
xxx
Jump 0x0222
. . . .
Correction for segment 1
Correction for segment 2

Fig. 5C